

### **Features**

- Hot Pluggable SFP form factor
- Operating data rate 1.25Gbps
- Single +3.3V power supply
- BIDI LC/UPC type pluggable optical interface
- 1310nm FP laser transmitter and PIN photo-detector
- Up to 20km on 9/125µm SMF
- Low power dissipation
- Metal enclosure, for lower EMI
- Support Digital Diagnostic Monitoring interface
- Commercial temperature range 0°C to 70°C

# **Compliance**

- SFP MSA
- Compliant with SFP Electrical MSA SFF-8431
- Compliant with SFP Mechanical MSA SFF-8432
- SFF-8472
- IEEE 802.3ae
- RoHS

# **Applications**

- Switches with SFP ports
- Router with SFP Ports
- Server or Network Adapter Card
- Optical Transmission System
- Other devices with SFP Ports



## **Description**

The 1G-SFP-U35-20 is a high-performance BiDi SFP transceiver designed for Gigabit Ethernet applications over SMF. Operating at wavelengths of 1310nm for transmitting data and 1550nm for receiving data, it enables bidirectional communication over a single fiber strand. Compliant with the IEEE 802.3z Gigabit Ethernet standard and SFP MSA, this transceiver supports data rates of up to 1.25Gbps and achieves transmission distances of up to 20 kilometers. It is ideal for point-to-point links, metropolitan area networks, and enterprise network backbones.

With its hot-pluggable design and low power consumption, the 1G-SFP-U35-20 ensures easy installation and energy efficiency. It also features DDM for real-time performance tracking, simplifying network management and troubleshooting. Whether deployed in data centers, telecommunications, or enterprise networks, this transceiver provides a cost-effective and reliable solution for medium-distance bidirectional communication needs.

## **Product performance Specifications**

#### 1. Basic Product Characteristics

Parameter	Symbol	Min	Тур.	Max	Unit
Storage Temperature	Ts	-40	-	85	°C
Power Supply Voltage	$V_{CC}$	-0.3	-	3.6	V
Relative Humidity (non-condensation)	RH	5	-	95	%
Damage Threshold	THd	5	-		dBm
Operating Case Temperature	Top	0	-	+70	°C
Power Supply Voltage	Vcc	3.135	3.3	3.465	V
Data Rate	-		1.25		Gb/s
Control Input Voltage High	-	2	-	Vcc	V
Control Input Voltage Low	-	0	-	0.8	V
Link Distance (SMF)	D	-	-	20	km

## 2. Product Optical and Electrical Characteristics

Parameter	Parameter Symbol Min		Тур.	Max	Unit
	Trans	mitter			
Single-ended Input Voltage Tolerance	Vcc	-0.3	-	4.0	V
Differential Input Voltage Swing	Vin,pp	200	-	2400	mVpp
Differential Input Impedance	Zin	90	100	110	Ohm
Transmit Disable Assert Time	-	-	-	5	us



Transmit Disable Voltage	Vdis	V <sub>CC</sub> -1.3	-	$V_{CC}$	V		
Transmit Enable Voltage	Ven	V <sub>EE</sub> -0.3	-	0.8	V		
Power Consumption	Р	-	-	1.0	W		
Supply Current	Icc	-	-	280	mA		
Center Wavelength	$\lambda_{\text{C}}$	1260	1310	1360	nm		
Spectrum Bandwidth(RMS)	σ	-	-	3.5	nm		
Average Optical Power <sub>1</sub>	P <sub>AVG</sub>	-9	-	-3	dBm		
Optical Extinction Ratio	ER	9	-		dB		
Transmitter OFF Output Power	P <sub>OFF</sub>	-	-	-45	dBm		
Transmitter Eye Mask <sub>2</sub>		Compliant with 802.3z(class 1 laser safety)					
	Rec	eiver					
Differential Output Voltage Swing	Vout,pp	500	-	900	mVpp		
Differential Output Impedance	Zout	90	100	110	Ohm		
Data output rise/fall time	Tr/Tf	-	100	-	ps		
LOS Assert Voltage	VlosH	V <sub>CC</sub> -1.3	-	Vcc	V		
LOS De-assert Voltage	VlosL	V <sub>EE</sub> -0.3	-	0.8	V		
Center Wavelength	λο	1530	1550	1570	nm		
Receiver Sensitivity (Average Power) <sub>3</sub>	Sen.	-	-	-20	dBm		
					dBm		
Input Saturation Power (overload)	Psat	-3	-	_	abiii		
Input Saturation Power (overload)  LOS Assert4	Psat LOSA	-3 -36	-	-	dBm		
				- - -21			
LOS Assert <sub>4</sub>	LOSA	-36			dBm		

Note1: Measure at 2^7-1 NRZ PRBS pattern

Note2: Transmitter eye mask definition

Note3: Measured with Light source 1310nm, ER=9dB; BER =<10^-12 @PRBS=2^7- 1 NRZ

Note4: When LOS de-asserted, the RX data+/- output is High-level (fixed)



# **Recommended Host Board Power Supply Circuit**

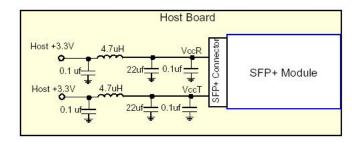


Figure 1:Recommended Host Board Power Supply Circuit

## **Recommended Interface Circuit**

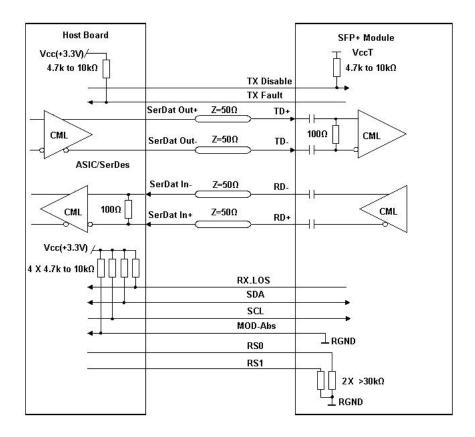


Figure2:Recommended Interface Circuit



# **Pin-out Definition**

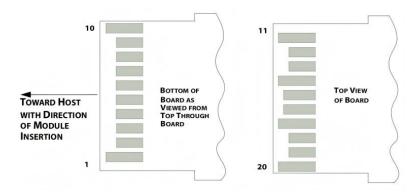


Figure3:Pin view

# **Pin Function Definitions**

Pin	Logic	Symbol	Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	TX_Fault	Module Transmitter Fault	2
3	LVTTL-I	TX_Disable	Transmitter Disable; Turns off transmitter laser output	3
4	LVTTL-I/O	SDA	2-wire Serial Interface Data Line (Same as MOD-DEF2 as defined in the INF-8074i)	4
5	LVTTL-I/O	SCL	2-wire Serial Interface Clock (Same as MOD-DEF1 as defined in the INF-8074i)	4
6		MOD_ABS	Module Absent, connected to VeeT or VeeR in the module	5
7	LVTTL-I	RS0	Adaptive multi-rate operation	6
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication (In FC designated as RX_LOS, in SONET designated as LOS, and in Ethernet designated at Signal Detect)	2
9	LVTTL-I	RS1	Adaptive multi-rate operation	6
10		VeeR	Module Receiver Ground	1
11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Non-Inverted Data Output	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Transmitter 3.3 V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1



Note1: The module signal ground pins, VeeR and VeeT, shall be isolated from the module case.

**Note2:** This pin is an open collector/drain output pin and shall be pulled up with  $4.7k\Omega-10k\Omega$  to Host\_Vcc on the host board. Pull ups can be connected to multiple power supplies, however the host board design shall ensure that no module pin has voltage exceeding module VccT/R + 0.5V.

Note3: This pin is an open collector/drain input pin and shall be pulled up with  $4.7k\Omega-10k\Omega$  to VccT in the module.

Note4: See SFF-8431 4.2 2-wire Electrical Specifications.

**Note5:** This pin shall be pulled up with  $4.7k\Omega-10k\Omega$  to Host\_Vcc on the host board.

**Note6:** Connect with  $30k\Omega$  load pulled down to GND in the module.

## **Monitoring Specification**

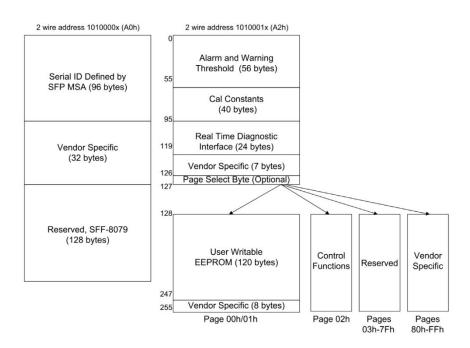


Figure4:Memory map

## **Memory map Table**

A0h	Bytes	Name	Description	
	A0h ID Fields			
0	1	Identifier	Type of transceiver	
1	1	Ext. Identifier	Extended identifier of type of transceiver	
2	1	Connector	Code for connector type	
3-10	8	Transceiver	Code for electronic or optical compatibility	
11	1	Encoding	Code for high speed serial encoding algorithm	
12	1	Signaling Rate, Nominal	Nominal signaling rate, units of 100 MBd.	
13	1	Rate Identifier	Type of rate select functionality	
14	1	Length (SMF,km) or Copper Cable Attenuation	Link length supported for single-mode fiber, units of km, or copper cable attenuation in dB at 12.9 GHz	



15	1	Length (SMF) or Copper Cable	Link length supported for single-mode fiber, units of 100 m, or copper
		Attenuation	cable attenuation in dB at 25.78 GHz
16	1	Length (50 um, OM2)	Link length supported for 50 um OM2 fiber, units of 10 m
17	1	Length (62.5 um, OM1)	Link length supported for 62.5 um OM1 fiber, units of 10 m
18	1	Length (OM4 or copper cable)	Link length supported for 50um OM4 fiber, units of 10 m.
			Alternatively, copper or direct attach cable, units of m
19	1	Length (OM3) or Cable length,	Link length supported for 50 um OM3 fiber, units of 10 m.
		additional	Alternatively, copper or direct attach cable multiplier and base value
20-35	16	Vendor name	SFP vendor name (ASCII)
36	1	Transceiver	Code for electronic or optical compatibility
37-39	3	Vendor OUI	SFP vendor IEEE company ID
40-55	16	Vendor PN	Part number provided by SFP vendor (ASCII)
56-59	4	Vendor rev	Revision level for part number provided by vendor (ASCII)
60-61	2	Wavelength	Laser wavelength (Passive/Active Cable Specification Compliance)
62	1	Fibre Channel Speed 2	Transceiver's Fibre Channel speed capabilities
63	1	CC_BASE	Check code for Base ID Fields (addresses 0 to 62)
64-65	2	Options	Indicates which optional transceiver signals are implemented
66	1	Signaling Rate, max	Upper signaling rate margin, units of %
67	1	Signaling Rate, min	Lower signaling rate margin, units of %
68-83	16	Vendor SN	Serial number provided by vendor (ASCII)
84-91	8	Date code	Vendor's manufacturing date code
92	1	Diagnostic Monitoring Type	Indicates which type of diagnostic monitoring is implemented (if any) in the transceiver
93	1	Enhanced Options	Indicates which optional enhanced features are implemented
30		Elinancea Options	(if any) in the transceiver
94	1	SFF-8472 Compliance	Indicates which revision of SFF-8472 the transceiver complies with.
95	1	CC_EXT	Check code for the Extended ID Fields (addresses 64 to 94)
96-127	32	Vendor Specific	Vendor Specific EEPROM
128-255	128	Reserved	Reserved (was assigned to SFF-8079)
		A	2h ID Fields
00-01	2	Temp High Alarm	MSB at low address
02-03	2	Temp Low Alarm	MSB at low address
04-05	2	Temp High Warning	MSB at low address
06-07	2	Temp Low Warning	MSB at low address
08-09	2	Voltage High Alarm	MSB at low address
10-11	2	Voltage Low Alarm	MSB at low address
12-13	2	Voltage High Warning	MSB at low address
14-15	2	Voltage Low Warning	MSB at low address
16-17	2	Bias High Alarm	MSB at low address
18-19	2	Bias Low Alarm	MSB at low address
20-21	2	Bias High Warning	MSB at low address
22-23	2	Bias Low Warning	MSB at low address

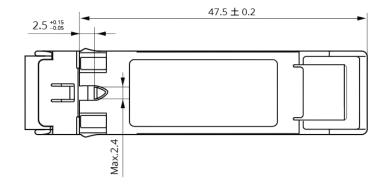


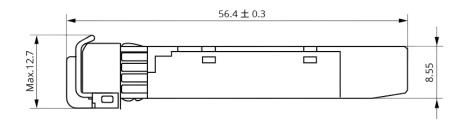
24-25 2	TX Power High Alarm	MSB at low address				
26-27 2	TX Power Low Alarm	MSB at low address				
28-29 2	TX Power High Warning	MSB at low address				
30-31 2	TX Power Low Warning	MSB at low address				
32-33 2	RX Power High Alarm	MSB at low address				
34-35 2	RX Power Low Alarm	MSB at low address				
36-37 2	RX Power High Warning	MSB at low address				
38-39 2	RX Power Low Warning	MSB at low address				
40-41 2	Optional Laser Temp High Alarm	MSB at low address				
42-43 2	Optional Laser Temp Low Alarm	MSB at low address				
44-45 2	Optional Laser Temp High Warning	MSB at low address				
46-47 2	Optional Laser Temp Low Warning	MSB at low address				
48-49 2	Optional TEC Current High Alarm	MSB at low address				
50-51 2	Optional TEC Current Low Alarm	MSB at low address				
52-53 2	Optional TEC Current High Warning	MSB at low address				
54-55 2	Optional TEC Current Low Warning	MSB at low address				
		Diagnostic calibration constants for optional External Calibration if				
56-91 36	Ext Cal Constants or Additional	External Calibration bit, A0h, byte 92, bit 4 is 1				
30-91 30	Enhanced Features	Additional Enhanced Features advertisement, control and status if				
		External Calibration bit, A0h, byte 92, bit 4 is 0				
92-94 3	Reserved					
95 1	CC_DMI	Check code for Base Diagnostic Fields (addresses 0 to 94)				
96-105 10	Diagnostics	Diagnostic Monitor Data (internally or externally calibrated)				
106-109 4	Optional Diagnostics	Monitor Data for Optional Laser temperature and TEC current				
110 1	Status/Control	Optional Status and Control Bits				
111 1	Reserved	Reserved (was assigned to SFF-8079)				
112-113 2	Alarm Flags	Diagnostic Alarm Flag Status Bits				
114 1	Tx Input EQ control	Tx Input equalization level control				
115 1	Rx Out Emphasis	Rx Output emphasis level control				
110	control	TXX Output emphasis level control				
116-117 2	Warning Flags	Diagnostic Warning Flag Status Bits				
118-119 2	Ext Status/Control	Extended module control and status bytes				
120-126 7	Vendor Specific	Vendor specific memory addresses				
127 1	Table Select	Optional Page Select				
	A2h Page 00-01h					
128-247 120	User EEPROM	User writable non-volatile memory				
248-255 8	Vendor Control	Vendor specific control addresses				
	A	2h Page 02h				
128-129 2	Reserved	Reserved for SFF-8690 (Tunable Transmitter)				
130 1	Reserved	Reserved for future receiver controls				
131 1	Rx Decision	RDT value setting				

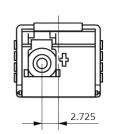


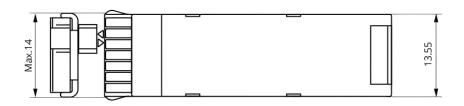
132-172	41	Reserved	Reserved for SFF-8690
173-255	83	Reserved	Reserved

# **Mechanical Dimension**









Unit: mm
Unspecified Tolerance: ±0.15mm



#### **Test Center**

#### 1. Performance Testing

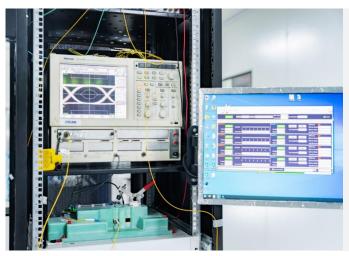
Every fiber optic transceiver is thoroughly tested by the LSOLINK Assurance Program, which is equipped with the world's most advanced analytical equipment to ensure that our transceivers meet the industry's international public protocol standards while still functioning flawlessly in your facility.



#### **Optical Spectrum Inspection**

Using the industry's leading optical spectrum analyser to check in real time that the parameters of the optical transceiver's laser comply with industry standards.

- Peak: Peak wavelength and peak level
- > 2nd Peak: Side-mode wavelength and level
- > Mean WI: Center wavelength
- > Total Power: Total power of spectrum
- SMSR: Side-Mode Suppression Ratio



### **Optical Signal Quality Inspection**

Using highly efficient sampling oscilloscopes and BERT testers, equipped with an automated test platform to accurately test the signal quality of the transceiver, test records are kept for up to 5 years to ensure the traceability of each transceiver.

- Eye Mask Margin(NRZ)
- > TDECQ(PAM4):transmitter dispersion eye closure
- > OMA: Optical modulation amplitude
- **BER:** Bit error rate
- ER: Extinction Ratio



#### **Flow Pressure Test**

Using multi-protocol network traffic analyser with various brands of switches to test the transceiver's ability to transmit at full speed.

- **Bandwidth:** Actual transceiver bandwidth on the port
- Packet Loss
- Packet Errors:CRC Errors/PCS Errors/Symbol Errors
- LinkDown Counts
- > latency

Aboveis part of our test bed network equipment. For more information, Please click <u>download</u> for optical transceiver performance test report.



## 2. Quality Control

We adopt advanced quality management solutions. Each transceiver is self-inspected, including:20x microscope inspection, 200x microscope inspection, and QC process inspection.



visual inspection



Microscopic inspection: 20X



Microscopic inspection: 200X



**Reliability Verification** 



**Optical endface inspection** 



**OQC Inspection** 



## 3. Compatibility Testing

Each optical transceiver is tested in LSOLINK's library of compatibility test equipment to ensure perfect compatibility with multiple brands on the market.





Arista DCS-7060SX2-48YC6-R



Cisco Nexus N9K-C9318YC-EX

Dell S4048-ONR





Huawei S6720-30L-HI-24S

Juniper QFX5110-48S-4C

Aboveis part of our test bed network equipment. For more information, Please click download to get the compatibility test report of each brand of optical transceiver.



# **Order Information**

Part Number	Description
1G-SFP-U35-20	1000BASE-BX SFP BIDI TX-1310nm/RX-1550nm 20km DOM LC SMF Transceiver Module
1G-SFP-D53-20	1000BASE-BX SFP BIDI TX-1550nm/RX-1310nm 20km DOM LC SMF Transceiver Module
1G-SFP-U34-20	1000BASE-BX SFP BIDI TX-1310nm/RX-1490nm 20km DOM LC SMF Transceiver Module
1G-SFP-D43-20	1000BASE-BX SFP BIDI TX-1490nm/RX-1310nm 20km DOM LC SMF Transceiver Module
1G-SFP-U35-40	1000BASE-BX SFP BIDI TX-1310nm/RX-1550nm 40km DOM LC SMF Transceiver Module
1G-SFP-D53-40	1000BASE-BX SFP BIDI TX-1550nm/RX-1310nm 40km DOM LC SMF Transceiver Module
1G-SFP-U45-80	1000BASE-BX SFP BIDI TX-1490nm/RX-1550nm 80km DOM LC SMF Transceiver Module
1G-SFP-D54-80	1000BASE-BX SFP BIDI TX-1550nm/RX-1490nm 80km DOM LC SMF Transceiver Module



# **Further Information**

Lighting the Path to Global Links

- Web | www.lsolink.com
- ☑ Email | For Sales@lsolink.com

# **Disclaimer**

- We are committed to continuous product improvement and feature upgrades, and the contents cont ained in this manual are subject to change without notice.
- 2. Nothing herein should be construed as constituting an additional warranty.
- LSOLINK assumes no responsibility for the use or reliability of equipment or software not provided by LSOLINK. Copyright LSOLINK.COM All Rights