

### **Features**

- Supporting 40Gbps
- Available length range 1~50m
- Active Optical Cable
- Operating data rate 40.125Gbps
- Single +3.3V power supply
- Max power dissipation 40G≤1.4W, 10G≤0.8W
- 850nm VCSEL Transmitter and PIN Receiver
- High-Density QSFP 38-PIN and 4x SFP 20-PIN
   Connector
- 4-Channel Full-Duplex Active Optical Cable
- Commercial temperature range 0°C to 70°C

## **Compliance**

- Compliant with SFP MSA and QSFP MSA
- Compliant with Electrical MSA SFF-8636 and SFF8431
- Compliant with Mechanical MSA SFF-8665 and SFF8432
- IEEE 802.3bj
- RoHS

## **Applications**

- 40/10Gigabit Ethernet
- Switches, servers, routers and HBA
- Data center cabling infrastructure
- High speed multi-channel parallel data connections



### **Description**

The 40G-QSFP-4S-A Active Optical Cable (AOC) is a high-performance, point-to-multipoint interconnect solution designed for 40GBASE Ethernet, storage networks, and high-performance computing environments. Featuring a QSFP+ connector on one end and four individual SFP+ connectors on the other, this cable splits a single 40 Gb/s link into four independent 10 Gb/s channels, enabling flexible connectivity between 40G and 10G infrastructure. Its ribbon-style optical design ensures reliable, low-latency data transmission over extended distances, making it ideal for high-density deployments in modern data centers and enterprise networks.

The 40G-QSFP-4S-A combines cost efficiency with plug-and-play simplicity, eliminating the need for signal retiming or external amplification. Its fan-out architecture streamlines the integration of 40G switches with 10G devices such as servers, storage arrays, and legacy infrastructure, reducing upgrade costs and complexity. Engineered for demanding applications like Al/ML clusters, cloud computing, and distributed storage systems, this AOC delivers robust signal integrity, scalability, and future-ready performance for evolving network demands.

### **Product performance Specifications**

#### 1. Basic Product Characteristics

Parameter	Symbol	Min	Тур.	Max	Unit
Storage Temperature	Ts	-40		85	°C
Operating Case Temperature	T <sub>C</sub>	0		70	°C
Relative Humidity	RH	0		85	%
Power Supply Voltage	Vcc	3.135	3.3	3.465	V
Data Rate Per Lan	DR	1		10	Gbp/s

### 2. Product Optical and Electrical Characteristics

Parameter	Symbol	Min	Тур.	Max	Unit	Note
		Transmitter				
Center Wavelength	С	840	850	860	nm	
RMS spettral width		-7.5		0.65	nm	
Average launch power, each lane	Pout			2.5	dBm	
Difference in launch power between any two lanes (OMA)				4	dB	
Extinction Ratio	ER	3			dB	



Peak power, each lane				4	dBm	
Transmitter and dispersion penalty (TDP), each lane	TDP			3.5	dB	
Average launch power of OFF transmitter, each lane				-30	dB	
		Receiver				
Stressed receiver sensitivity in OMA, each lane	С	840	850	860	nm	
Maximum Average power at receiver input, each lane				-5.4	dBm	1
Receiver Reflectance				2.4	dBm	
Peak power, each lane				-12	dB	
LOS Assert					dBm	
LOS De-Assert – OMA				-7.5	dBm	1
LOS Hysteresis					dB	

**Note1:** Measured with conformance test signal at TP3 for BER =  $10e^{-12}$ 



## **Recommended Host Board Power Supply Circuit**

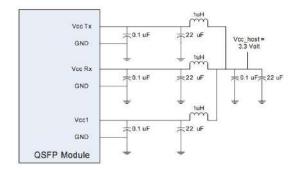


Figure 1:Recommended Host Board Power Supply Circuit

### **Recommended Interface Circuit**

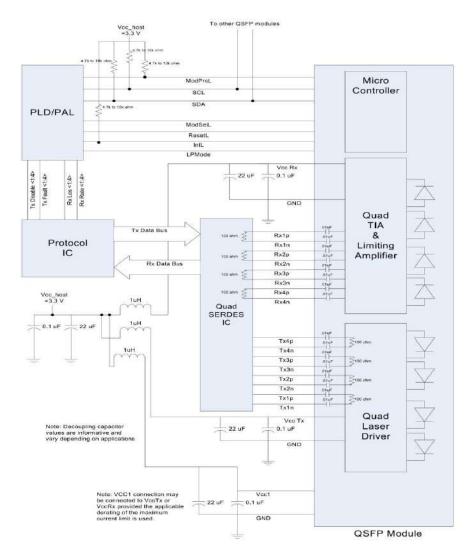


Figure2:Recommended Interface Circuit



## **QSFP Pin-out Definition**

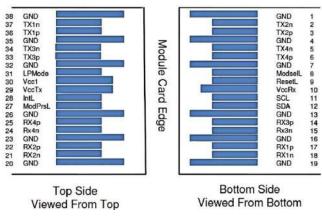


Figure3:QSFP Pin view

### **QSFP Pin Function Definitions**

Pin	Logic	Symbol	Description	Note
1		GND	Ground	
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3
7		GND	Ground	1
8	LVTTL-I	ModSelL	Module Select	4
9	LVTTL-I	ReSelL	Module Select	4
10		Vcc Rx	+3.3V Power Supply Receiver	
11	LVCMOS-I/O	SCL	2-wire serial interface clock	
12	LVCMOS-I/O	SDA	2-wire serial interface data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3
15	CML-O	Rx3n	Receiver Inverted Data Output	3
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3
18	CML-O	Rx1n	Receiver Inverted Data Output	3
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3



25	CML-O	Rx4p	Receiver Non-Inverted Data Output Ground	3
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	4
28	LVTTL-O	IntL	Interrupt	4
29		Vcc Tx	+3.3V Power supply transmitter	2
30		Vcc1	+3.3V Power supply	2
31	LVTTL-I	LPMode	Low Power Mode	4
32		GND	Ground	1
33	CML-I	Тх3р	Transmitter Non-Inverted Data Input	3
34	CML-I	Tx3n	Transmitter Inverted Data Input	3
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3
37	CML-I	Tx1n	Transmitter Inverted Data Input	3
38		GND	Ground	1

**Note1:** GND is the symbol for signal and supply (power) common for the QSFP+ module. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.

Note2: Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Requirements defined for the host side of the Host Edge Card Connector are listed in Table. Recommended host board power supply filtering is shown in Host board power supply circuit. Vcc Rx Vcc1 and Vcc Tx may be internally connected within the QSFP module in any combination. The connector pins are each rated for a maximum current of 500 mA.

Note3: High-speed signal interfaces require differential pairs (e.g. TX1+/TX1-) with tightly matched impedances (typically 100Ω).

Note4: The management and control signals are based on LVTTL level logic and are used for functions such as module selection and reset.

### **SFP Pin-out Definition**

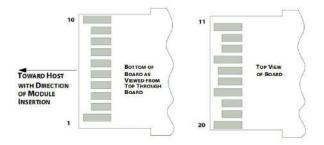


Figure4:SFP Pin view

## **SFP Pin Function Definitions**

Pin	Logic	Symbol	Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	TX_Fault	Module Transmitter Fault	2
3	LVTTL-I	TX_Disable	Transmitter Disable; Turns off transmitter laser output	



4	LVTTL-I/O	SDA	2-wire Serial Interface Data Line (Same as MOD-DEF2 as defined in the INF-8074i)	4
5	LVTTL-I/O	SCL	2-wire Serial Interface Clock (Same as MOD-DEF1 as defined in the INF-8074i)	4
6		MOD_ABS	Module Absent, connected to VeeT or VeeR in the module	5
7	LVTTL-I	RS0	Adaptive multi-rate operation	6
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication (In FC designated as RX_LOS, in SONET designated as LOS, and in Ethernet designated at Signal Detect)	2
9	LVTTL-I	RS1	Adaptive multi-rate operation	6
10		VeeR	Module Receiver Ground	1
11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Non-Inverted Data Output	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Transmitter 3.3 V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1

Note1: The module signal ground pins, VeeR and VeeT, shall be isolated from the module case.

**Note2:** This pin is an open collector/drain output pin and shall be pulled up with  $4.7k\Omega-10k\Omega$  to Host\_Vcc on the host board. Pull ups can be connected to multiple power supplies, however the host board design shall ensure that no module pin has voltage exceeding module VccT/R + 0.5V.

**Note3:** This pin is an open collector/drain input pin and shall be pulled up with  $4.7k\Omega-10k\Omega$  to VccT in the module.

Note4: See SFF-8431 4.2 2-wire Electrical Specifications.

**Note5:** This pin shall be pulled up with  $4.7k\Omega-10k\Omega$  to Host\_Vcc on the host board.

Note6: Connect with  $30k\Omega$  load pulled down to GND in the module.



## **QSFP Monitoring Specification**

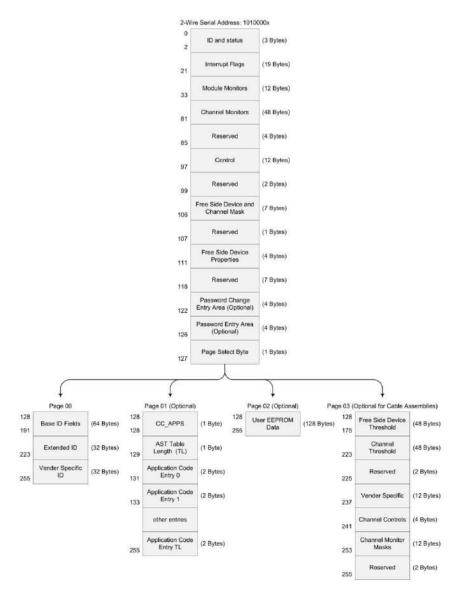


Figure5:QSFP Memory map

## **QSFP Memory map Table**

Byte	Unit	Name	Description		
	Lower Page 00h				
0	1	Identifier	Type of transceiver,Page 00h Byte 0 and Page 00h Byte 128 shall contain the same parameter values.		
1	1	Status	Revision Compliance		
2	1	Status	Status indicators		
3-21	19	Interrupt Flags	Consist of interrupt flags for LOS, Tx Fault, warnings and alarms. The non-asserted state shall be 0b.		



22	1	Temperature MSB	Internally measured temperature (MSB)
23	1	Temperature LSB	Internally measured temperature (LSB)
24-25	2	Reserved	Reserved
26	1	Supply Voltage MSB	Internally measured supply voltage (MSB)
27	1	Supply Voltage LSB	Internally measured supply voltage (LSB)
28-29	2	Reserved	Reserved
30-33	4	Vendor Specific	Vendor Specific
34	1	Rx1 Power MSB	
35	1	Rx1 Power LSB	Internally measured Rx1 input power
36	1	Rx2 Power MSB	
37	1	Rx2 Power LSB	Internally measured Rx2 input power
38	1	Rx3 Power MSB	
39	1	Rx3 Power LSB	Internally measured Rx3 input power
40	1	Rx4 Power MSB	
41	1	Rx4 Power LSB	Internally measured Rx4 input power
42	1	Tx1 Bias MSB	ladama libana a a anna d'Tard bia a
43	1	Tx1 Bias LSB	Internally measured Tx1 bias
44	1	Tx2 Bias MSB	Internally management TvO bigs
45	1	Tx2 Bias LSB	Internally measured Tx2 bias
46	1	Tx3 Bias MSB	Internally management Tv2 king
47	1	Tx3 Bias LSB	Internally measured Tx3 bias
48	1	Tx4 Bias MSB	Internally measured Tx4 bias
49	1	Tx4 Bias LSB	internally measured 134 bias
50	1	Tx1 Power MSB	Internally measured Tx1 Power
51	1	Tx1 Power LSB	internally incasured 1X11 ower
52	1	Tx2 Power MSB	Internally measured Tx2 Power
53	1	Tx2 Power LSB	internally measured TAZ T GWei
54	1	Tx3 Power MSB	Internally measured Tx3 Power
55	1	Tx3 Power LSB	internally measured the Fewer
56	1	Tx4 Power MSB	Internally measured Tx4 Power
57	1	Tx4 Power LSB	
58-65	8	Reserved	Reserved channel monitor set 4
66-73	8	Reserved	Reserved channel monitor set 5
74-81	8	Vendor Specific	Vendor Specific
82-85	4	Reserved	Reserved
86-99	14	Control	Control
100-106	7	Free Side Device and Channel Masks	Free Side Device and Channel Masks
107-110	4	Free Side Device Properties	Free Side Device Properties
			Used for:
111-112	2	2 Assigned for use by PCI Express	- The PCI Express External Cable Specification
			- The PCI Express OCuLink Specification



110 117	4	Free Side Device Proporties	Free Side Davise Preparties
113-117	4	Free Side Device Properties Reserved	Free Side Device Properties Reserved
118	1		
119-122	4	Password Change Entry Area	Password Change Entry Area Password Entry Area
123-126	4	Password Entry Area	•
127	1	Page Select Byte	Page Select Byte
		Up	per Page 00h
128	1	Identifier	Identifier Type of free side device.(See SFF-8024 Transceiver Management)
			Extended Identifier of free side device. Includes power classes, CLEI
129	1	Ext. Identifier	codes, CDR capability.
			Code for media connector type. (See SFF-8024 Transceiver
130	1	Connector Type	Management)
131-138	8	Specification Compliance	Code for electronic or optical compatibility.
	-		Code for serial encoding algorithm. (See SFF-8024 Transceiver
139	1	Encoding	Management)
			Nominal signaling rate, units of 100 MBd. For rate > 25.4 GBd, set
140	1	Signaling rate, nominal	this to FFh and use Byte 222.
		Extended Rate Select	
141	1	Compliance	Tags for extended rate select compliance.
			Link length supported at the signaling rate in byte 140 or page 00h
142	1	Length (SMF)	byte 222, for SMF fiber in km *. A value of 1 shall be used for reaches
			from 0 to 1 km.
143	1	Length (OM3 50 um)	Link length supported at the signaling rate in byte 140 or page 00h
110	,	Longar (Owo oo am)	byte 222, for EBW 50/125 um fiber (OM3), units of 2 m *
144	1	Length (OM2 50 um)	Link length supported at the signaling rate in byte 140 or page 00h
	·	201.941 (011.2 00 4111)	byte 222, for 50/125 um fiber (OM2), units of 1 m *
		Length (OM1 62.5 um) or Copper	Link length supported at the signaling rate in byte 140 or page 00h
145	1	Cable Attenuation	byte 222, for 62.5/125 um fiber (OM1), units of 1 m *, or copper cable
			attenuation in dB at 25.78 GHz.
			Length of passive or active cable assembly (units of 1 m) or link
146	1	Length (passive copper or active	length supported at the signaling rate in byte 140 or page 00h byte
		cable or OM4 50 um)	222, for OM4 50/125 um fiber (units of 2 m) as indicated by Byte 147. See 6.3.12.
147	1	Device technology	Device technology
148-163	16	Vendor name	Free side device vendor name (ASCII)
164	10	Extended Module	Extended Module codes for InfiniBand.
165-167	3	Vendor OUI	Free side device vendor IEEE company ID.
168-183	16	Vendor PN	Part number provided by free side device vendor(ASCII)
184-185	2	Vendor rev	Revision level for part number provided by the vendor(ASCII)
101-100	-	TOTAL TOTAL	Nominal laser wavelength (wavelength=value/20 in nm) or copper
186-187	2	Wavelength or Copper Cable	cable attenuation in dB at 2.5 GHz (Byte 186) and 5.0 GHz (Byte
	_	Attenuation	187)
188-189	2	Wavelength tolerance or Copper	The range of laser wavelength (+/- value) from nominal wavelength.
		, inter-	, , , , , , , , , , , , , , , , , , ,



		Cable Attenuation	(wavelength Tol. =value/200 in nm) or copper cable attenuation in dB at 7.0 GHz (Byte 188) and 12.9 GHz (Byte 189)
400	4	Managara	
190	1	Max case temp	Maximum case temperature
191	1	CC_BASE	Check code for base ID fields (Bytes 128-190)
192	1	Link codes	Extended Specification Compliance Codes (See SFF-8024)
193-195	3	Options	Optional features implemented.
196-211	16	Vendor SN	Serial number provided by vendor.(ASCII)
212-219	8	Date Code	Vendor's manufacturing date code.
220	1	Diagnostic Monitoring Type	Indicates which type of diagnostic monitoring is implemented (if any) in the free side device. Bit 1,0 Reserved.
221	1	Enhanced Options	Indicates which optional enhanced features are implemented in the free side device.
222	1	CC_EXT	Check code for the Extended ID Fields (Bytes 192-222)
224-255	32	Vendor Specific	Vendor Specific EEPROM

## **SFP Monitoring Specification**

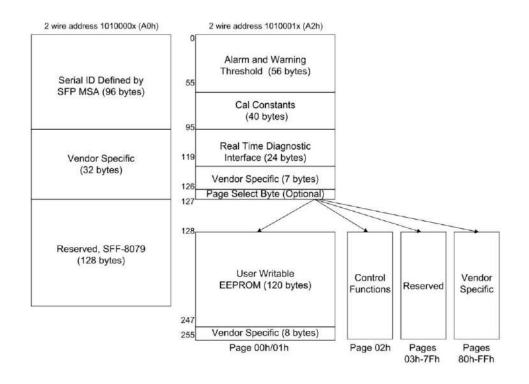


Figure6:SFP Memory map



## **SFP Memory map Table**

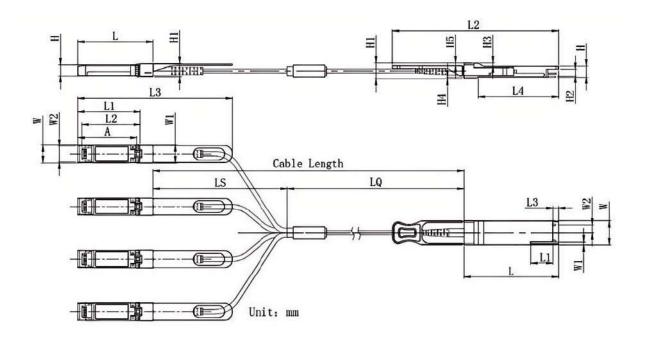
Byte	Unit	Name	Description		
A0h ID Fields					
0	1	Identifier	Type of transceiver		
1	1	Ext. Identifier	Extended identifier of type of transceiver		
2	1	Connector	Code for connector type		
3-10	8	Transceiver	Code for electronic or optical compatibility		
11	1	Encoding	Code for high speed serial encoding algorithm		
12	1	Signaling Rate, Nominal	Nominal signaling rate, units of 100 MBd.		
13	1	Rate Identifier	Type of rate select functionality		
14	1	Length (SMF,km) or Copper Cable Attenuation	Link length supported for single-mode fiber, units of km, or copper cable attenuation in dB at 12.9 GHz		
15	1	Length (SMF) or Copper Cable Attenuation	Link length supported for single-mode fiber, units of 100 m, or copper cable attenuation in dB at 25.78 GHz		
16	1	Length (50 um, OM2)	Link length supported for 50 um OM2 fiber, units of 10 m		
17	1	Length (62.5 um, OM1)	Link length supported for 62.5 um OM1 fiber, units of 10 m		
18	1	Length (OM4 or copper cable)	Link length supported for 50um OM4 fiber, units of 10 m.  Alternatively, copper or direct attach cable, units of m		
19	1	Length (OM3) or Cable length, additional	Link length supported for 50 um OM3 fiber, units of 10 m.  Alternatively, copper or direct attach cable multiplier and base value		
20-35	16	Vendor name	SFP vendor name (ASCII)		
36	1	Transceiver	Code for electronic or optical compatibility		
37-39	3	Vendor OUI	SFP vendor IEEE company ID		
40-55	16	Vendor PN	Part number provided by SFP vendor (ASCII)		
56-59	4	Vendor rev	Revision level for part number provided by vendor (ASCII)		
60-61	2	Wavelength	Laser wavelength (Passive/Active Cable Specification Compliance)		
62	1	Fibre Channel Speed 2	Transceiver's Fibre Channel speed capabilities		
63	1	CC_BASE	Check code for Base ID Fields (addresses 0 to 62)		
64-65	2	Options	Indicates which optional transceiver signals are implemented		
66	1	Signaling Rate, max	Upper signaling rate margin, units of %		
67	1	Signaling Rate, min	Lower signaling rate margin, units of %		
68-83	16	Vendor SN	Serial number provided by vendor (ASCII)		
84-91	8	Date code	Vendor's manufacturing date code		



92	1	Diagnostic Monitoring Type	Indicates which type of diagnostic monitoring is implemented (if any)	
			in the transceiver	
93	1	Enhanced Options	Indicates which optional enhanced features are implemented	
			(if any) in the transceiver	
94	1	SFF-8472 Compliance	Indicates which revision of SFF-8472 the transceiver complies with.	
95	1	CC_EXT	Check code for the Extended ID Fields (addresses 64 to 94)	
96-127	32	Vendor Specific	Vendor Specific EEPROM	
128-255	128	Reserved	Reserved (was assigned to SFF-8079)	



### **Mechanical Dimension**



#### Note:

- Diameter: 3mm
- Minimum bend radius:30mm
- Cable color:Orange(OM2),Aqua(OM3),Magenta(OM4)
- When L≤1m, the tolerance is +5cm
- When 1m≤L≤4.5m, the tolerance is +15cm
- When 5m≤L≤14.5m, the tolerance is +30cm
- When L≥15m, the tolerance is +2%m

#### Waring:

- The transceiver optics is supplied with a dust cover. This plug protects the transceiver optics during standard manufacturing processes by preventing contamination from air borne particles. It is recommended that the dust cover remain in the transceiver whenever an optical fiber connector is not inserted.
- Handling Precautions: This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended. Follow guidelines according to proper ESD procedures.
- Laser Safety: Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.



### **Test Center**

#### 1. Performance Testing

Every fiber optic transceiver is thoroughly tested by the LSOLINK Assurance Program, which is equipped with the world's most advanced analytical equipment to ensure that our transceivers meet the industry's international public protocol standards while still functioning flawlessly in your facility.



### **Optical Spectrum Inspection**

Using the industry's leading optical spectrum analyser to check in real time that the parameters of the optical transceiver's laser comply with industry standards.

- Peak: Peak wavelength and peak level
- > 2nd Peak: Side-mode wavelength and level
- > Mean WI: Center wavelength
- Total Power: Total power of spectrum
- SMSR: Side-Mode Suppression Ratio



### **Optical Signal Quality Inspection**

Using highly efficient sampling oscilloscopes and BERT testers, equipped with an automated test platform to accurately test the signal quality of the transceiver, test records are kept for up to 5 years to ensure the traceability of each transceiver.

- Eye Mask Margin(NRZ)
- > TDECQ(PAM4):transmitter dispersion eye closure
- > OMA: Optical modulation amplitude
- **BER:** Bit error rate
- ER: Extinction Ratio



### **Flow Pressure Test**

Using multi-protocol network traffic analyser with various brands of switches to test the transceiver's ability to transmit at full speed.

- **Bandwidth:** Actual transceiver bandwidth on the port
- Packet Loss
- Packet Errors:CRC Errors/PCS Errors/Symbol Errors
- LinkDown Counts
- > latency

Aboveis part of our test bed network equipment. For more information, Please click <u>download</u> for optical transceiver performance test report.



## 2. Quality Control

We adopt advanced quality management solutions. Each transceiver is self-inspected, including:20x microscope inspection, 200x microscope inspection, and QC process inspection.



visual inspection



**Microscopic inspection: 20X** 



Microscopic inspection: 200X



**Reliability Verification** 



**Optical endface inspection** 



**OQC Inspection** 



## 3. Compatibility Testing

Each optical transceiver is tested in LSOLINK's library of compatibility test equipment to ensure perfect compatibility with multiple brands on the market.



Arista DCS-7060SX2-48YC6-R



Aruba 8360-32Y4C



**Brocade ICX-7750-48F** 



Cisco Catalyst C9500-24Y4C



**Dell S4048-ON** 



**Extreme X670-G2-48X-4Q** 

Aboveis part of our test bed network equipment. For more information, Please click <u>download</u> to get the compatibility test report of each brand of optical transceiver.



## **Order Information**

Part Number	Length(m)	Connector Type	Cable Type	Cable Jacket
40G-QSFP-4S-A1	1	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A3	3	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A5	5	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A7	7	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A10	10	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A15	15	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A20	20	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A25	25	QSFP+ to 4xSFP+	Active Optical	OFNP
100G-QSFP-4S-A30	30	QSFP+ to 4xSFP+	Active Optical	OFNP



# **Further Information**

Lighting the Path to Global Links

Web | www.lsolink.com

☑ Email | For Sales@lsolink.com

# Disclaimer

- We are committed to continuous product improvement and feature upgrades, and the contents cont ained in this manual are subject to change without notice.
- 2. Nothing herein should be construed as constituting an additional warranty.
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